

In conjunction with the 19th Euromicro Int'l Conference on Real-Time Systems

Pisa, Italy, July 4 - 6, 2007
http://feanor.sssup.it/ecrts07/

7th Int'l Workshop on Worst-Case Execution Time (WCET) Analysis

Pisa, Italy - July 3, 2007

http://www.irit.fr/wcet2007

Workshop chair

Christine Rochange IRIT, Toulouse, France rochange@irit.fr

Important dates

Submission deadline: April 14

Notification: May 22 Submission of June 12

corrected papers:

Broadcast of papers June 20

to attendees:

WCET Workshop: July 3

ECRTS conference: July 4 - 6 Final version of Sept. 7

papers:

Program committee

Antoine Colin Rapita Systems Ltd.

Raimund Kirner *Technische Universität Wien*

Stefan Petters
National ICT Australia Ltd.

Christer Sandberg

Mälardalen University

Stephan Thesing Rücker GmbH

Steering committee

Guillem Bernat
University of York

Jan Gustafsson
Mälardalen University

Peter Puschner
Technical University of Vienna

The Euromicro Technical Committee organizes a number of satellite events attached to its 19th International Real-Time Systems Conference. This workshop is the seventh on the series of WCET workshops that started at the 2001 Euromicro conference.

The goal of the workshop is to bring together people from academia, tool vendors and users in industry that are interested in all aspects of timing analysis for real-time systems. The workshop will provide a relaxed forum to present and discuss new ideas, new research directions, and to review current trends in this area. The workshop will be based on short presentations that should encourage discussion by the attendees.

The topics of the workshop include any issue related to timing analysis, in particular:

- Different approaches at computing WCET
- Flow analysis for WCET
- Modeling and low-level of processor features
- Calculation methods for WCET
- Strategies to reduce the complexity of WCET analysis
- Integration of WCET and schedulability analysis
- Evaluation and case studies
- Testing methods for WCET analysis
- Tools for timing analysis
- Design for timing predictability
- Integration of WCET analysis into the development process
- Compiler optimizations for worst-case paths
- WCET analysis for multi-processors, multi-cores or SMTs
- WCET analysis for networks (e.g., CAN)

Statements which are innovative, controversial, or that present new approaches are specially sought.

Submission of papers:

People who would like to participate in this event are asked to submit a 6 page short paper (PS or PDF) **before April**, **14** via the workshop website. All papers will be made available to all participants a week before the workshop so that contributions can be examined prior to the workshop. Authors will be asked to produce a final version of their paper that includes the issues covered in the discussions by September 7, 2007. The final papers will be published in the workshop proceedings. The authors of the two best papers will be invited to submit expanded versions for publication in the Springer Real-Time Systems Journal.

The workshop fosters a highly interactive format of short presentations combined with ample time for in-depth discussions. Authors will be asked to produce a final version of their paper that includes the issues covered in the discussions by September 7, 2007. To foster the workshop character, the number of participants will be limited. There will be a small charge for workshop registration.