



### On the Existence of a Cyclic Schedule for Non-Preemptive Periodic Tasks with Release Offset

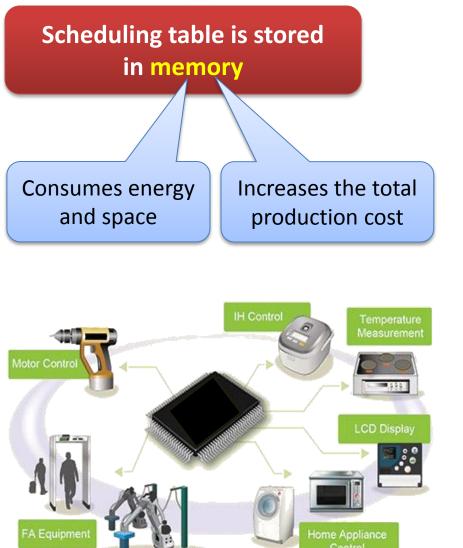
Mitra Nasri\*

**Emmanuel Grolleau** 



### **Motivations**





#### Arm Cortex MCU family

STM32 32-bit ARM C	Cortex MCUs <	STM32F	2 Series	STM32F3 S	eries	STM32F4	Series	STM32	F7 Series
fotal Parts: (752)	for STM32 32-bit	ARM Cor	tex MCU	s   Matching	Parts :	(90)			
Part Number	Package <b>T</b> 🖨	Core 🗢	Frequer	ng 🗘 ncy (MHz) sor speed)	FLAS Size (Prog		Interna RAM S (kB)	al <b>▼≑</b> Size	I/Os <b>▼ ≑</b> (High Current)
STM32L011G4	UFQFPN 28 4x4 x0.55	ARM Co rtex-M		32	16	5	2	2	24
STM32L011K4	LQFP 32 7x7x1. 4,	ARM Co rtex-M		32	16	5	2	2	28
STM32L021D4	TSSOP 14	ARM Co rtex-M		32	16	6	2	2	11
STM32L021F4	UFQFPN 20 3x3 x0.6	ARM Co rtex-M		32	16	5	2	2	16
STM32L021G4	UFQFPN 28 4x4 x0.55	ARM Co rtex-M		32	16	5		2	24
STM32L021K4	LQFP 32 7x7x1. 4	ARM Co rtex-M		32	16	5		2	28
STM32L031F4	TSSOP 20	ARM Co rtex-M		32	1	6		8	15
STM32L071C8	LQFP 48 7x7x1. 4	ARM Co rtex-M		32	6	4	2	20	37
STM32L071RZ	LQFP 64 10x10 x1.4,	ARM Co rtex-M		32	19	92	2	20	51
STM32L071VB	LQFP 100 14x1 4x1.4	ARM Co rtex-M		32	1	28		20	84

### **Finding an Observation Interval for Online Algorithms**

#### System model and assumptions

- Uniprocessor
- <u>Non-preemptive</u>
- Periodic tasks
- Constrained deadline
- Independent tasks

# We are interested in the approaches that build the table using an <u>online algorithm</u>

#### What is the length of cyclic schedule?

- Synchronous release: one hyperperiod
- Asynchronous release: depends on the scheduling algorithm

Work-conserving or Non-work-conserving?

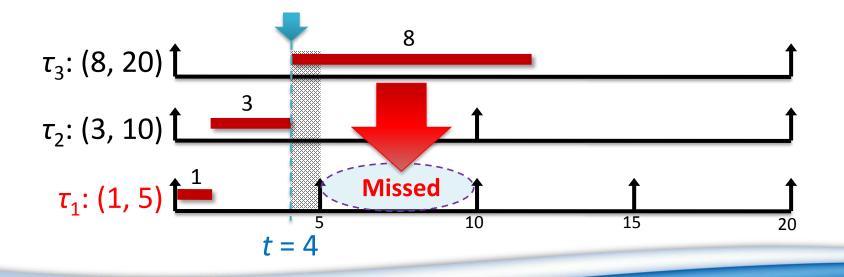
## **State-of-the-Art of Simulation Interval**

Processor	Deadlines	Dependency	dency Scheduling algorithm Simulation interval		Reference	
1	$D_i \leq T_i$	Independent	Fixed-task priority	$[0, O^{\max} + 2H)$	Leung and Merrill (1980)	
1	Arbitrary	Independent	Fixed-job priority	$[0, O^{\max} + 2H)$	Goossens and Devillers (1999)	
1	$D_i \leq T_i$	Independent	Fixed-job priority	$[0, S_n + H)$	Goossens and Devillers (1997)	
1	$D_i \leq T_i$	Mutual exclusion, simple precedence	Any work-conserving (with idle task)	$[0, \theta_c + H)$	Choquet-Geniet and Grolleau (2004), Bado et al. (2012)	
Uniform	$D_i \leq T_i$	Independent	Global fixed-task priority	$[0, S_n + H)$	Cucu and Goossens (2006)	
Unrelated	$D_i \leq T_i$	Independent	Global fixed-task priority	$[0, S_n + H)$	Cucu-Grosjean and Goossens (2011)	
Identical	Arbitrary	Independent	Global fixed-task priority	$[0, \hat{S}_n + H)$	Cucu and Goossens (2007)	
Identical	$D_i \leq T_i$	Independent	Any	$[0, O^{\max} + H \prod_{i=1}^{n} (C_i + 1))$	Baro et al. (2012), Nélis et al. (2013)	
Identical	$D_i \leq T_i$	Simple precedence	Any	$[0, O^{\max} + H \prod_{i=1}^{n} (C_i + 1))$	Baro et al. (2012)	
Identical	Arbitrary	Structural constraint	Any	$[0, H \prod_{i=1}^{n} ((O_i + D_i - T_i)_0 + 1))$	Goossens et al. (2016)	
	Im	practical!				
$\hat{s}_1 \doteq O_1$						
$\hat{s}_i \doteq \max\left(C_i\right)$	$O_i, O_i + \left\lceil \frac{\hat{s}_{i-1}}{n} \right\rceil$	$\left. \frac{O_i}{\Gamma_i} \right  T_i + H_i$	[0, H]	${n \choose i=1}((O_i + D_i - Z_i))$	$(i)_0 + 1))$	
$H_i \doteq \operatorname{lcm}_{j=1}$	$=1i(T_j)$					

J. Goossens, E. Grolleau, L. Cucu-Grosjean, Periodicity of real-time schedules for dependent periodic tasks on identical multiprocessor platforms, Real-Time Systems, 2016.

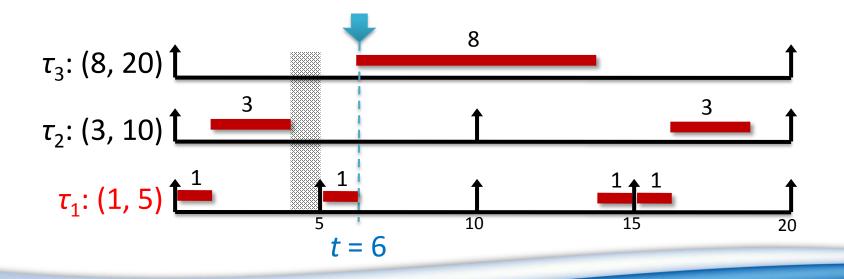
## **Non-Work-Conserving Scheduling**

- Leaves the processor idle even if there are tasks that are not yet scheduled
- Example: Precautious-RM [Nasri2014]
  - is an online algorithm
  - follows rate monotonic priorities
  - schedules the highest-priority task <u>only if</u> it will not cause a deadline miss for the next job of the task with the smallest period

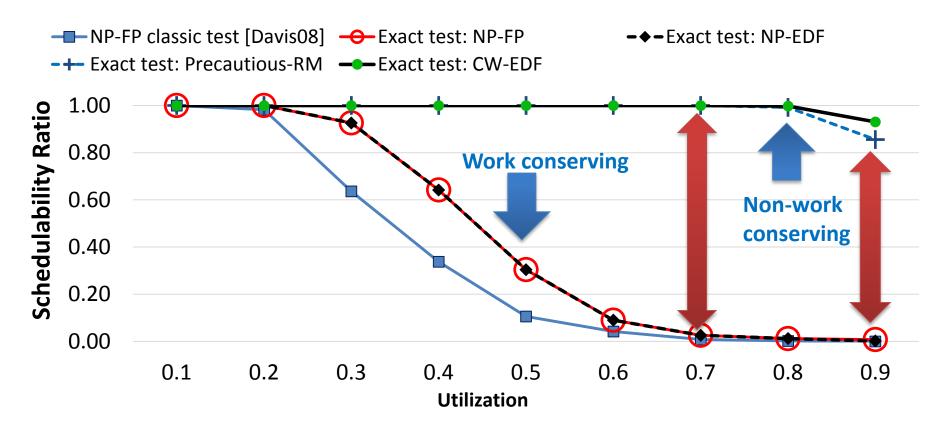


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### Why Non-Work-Conserving Algorithms are Needed?



• Autosar benchmark [Kramer2015] (only task set that satisfy the "necessary schedulability conditions").

• The exact test is from

Nasri and Brandenburg, "An Exact and Sustainable Analysis of Non-Preemptive Scheduling", manuscript, available at <a href="https://people.mpi-sws.org/~bbb/papers/pdf/preprint\_np\_exact\_analysis.pdf">https://people.mpi-sws.org/~bbb/papers/pdf/preprint\_np\_exact\_analysis.pdf</a>

## Problem 1

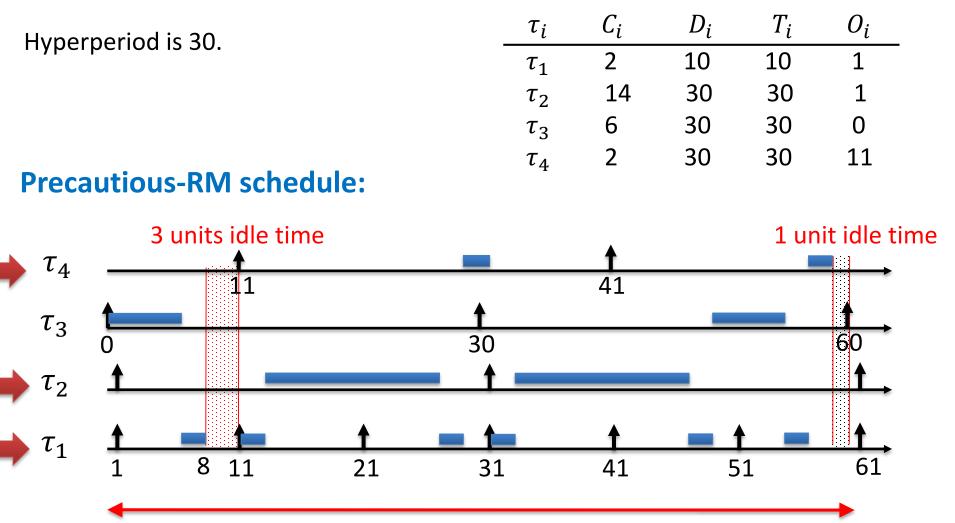
#### • Given:

- Uniprocessor
- Non-preemptive
- Periodic tasks
- Release offsets
- Constrained (or arbitrary) deadline
- Dependent (or independent)
- Scheduled by:
  - A non-work-conserving scheduling algorithm such as Precautious-RM

### Problem:

What is the length of a cyclic schedule? When does the cyclic schedule start?

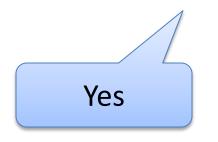
### **The Length of Cyclic Schedule**



Cycle is from 0 to 60

## **Other Open Problems**

Problem 3: Is there any feasible asynchronous task set \(\tau\) that does not have <u>any</u> cyclic schedule with length H?



### Problem 4:

Given: a feasible non-preemptive task set Find: schedule S that has the smallest cycle length

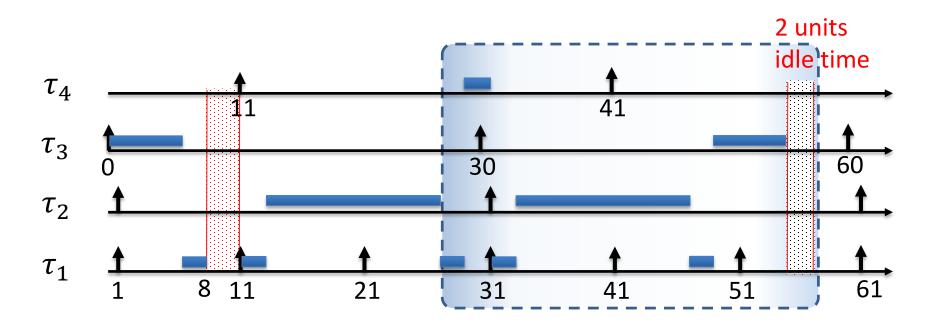
### **Problem 5:**

No

Given: a feasible non-preemptive task set Find: How to find/build that schedule?

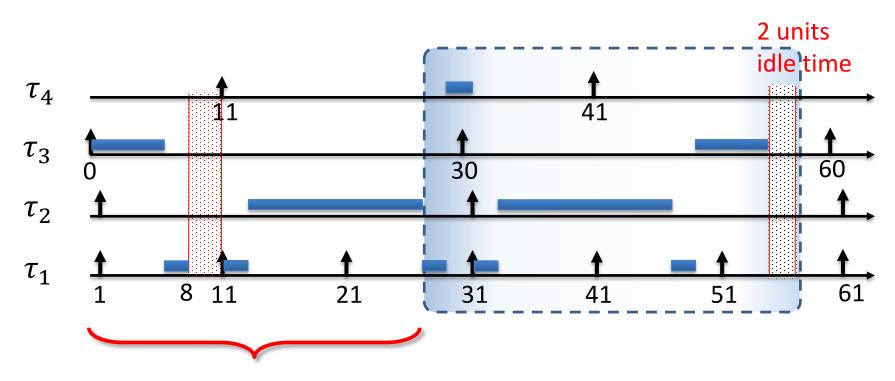
H = hyperperiod

Assume: task set is periodic with constrained deadline and independent tasks



### A cyclic schedule with length H exists.

### **Other Concerns**



### What about the length of non-cyclic schedule? What if it is even larger than the cyclic schedule?

# Summary



Small offline tables

#### Motivations

- The need of creating small offline tables to save memory
- Using online scheduling algorithms is an efficient option

#### Non-work-conserving non-preemptive scheduling

- Promising performance
- High system predictability

The length and start time of a cyclic schedule

Performance and

predictability

#### **Open Problems**

- No practical bound on the length of simulation interval
- We need methods to find the smallest cyclic schedule



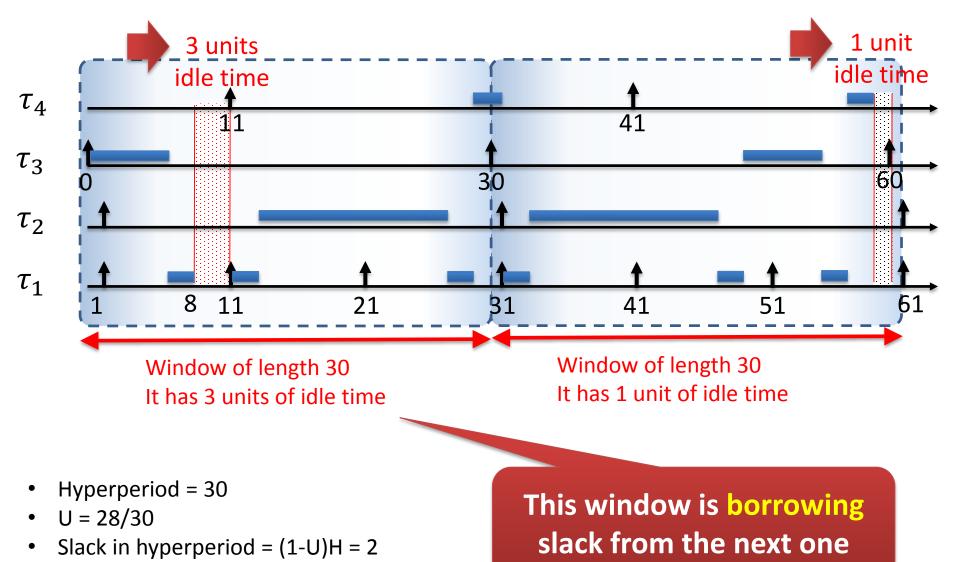
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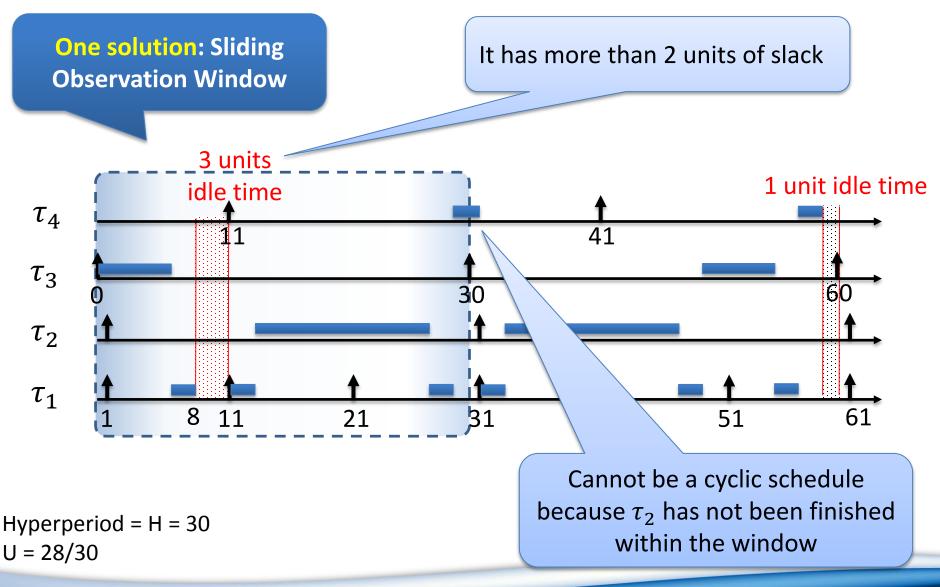


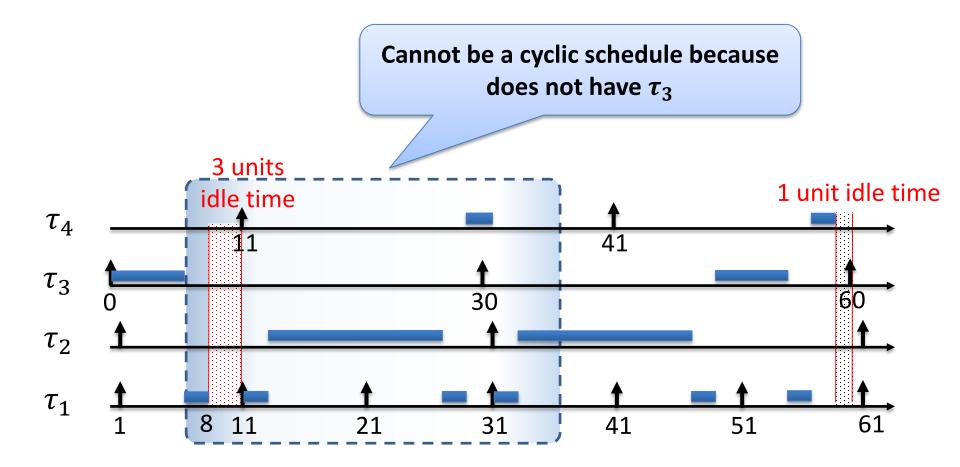
# Questions

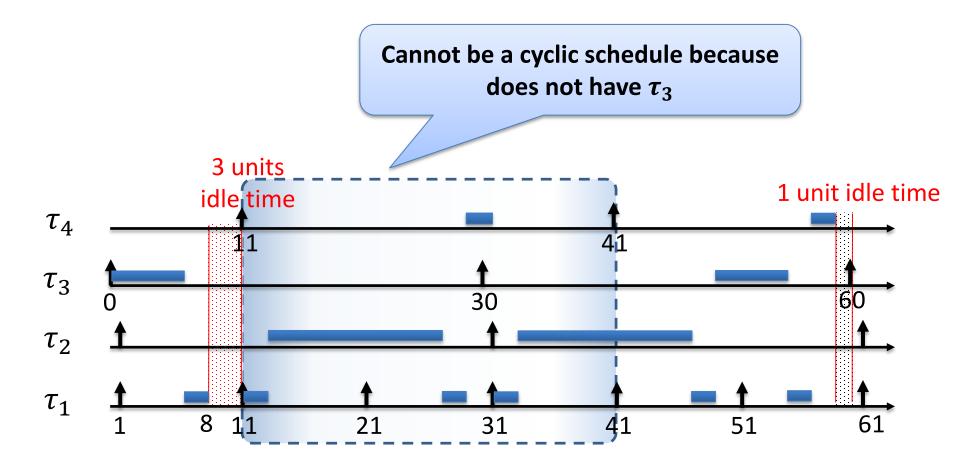


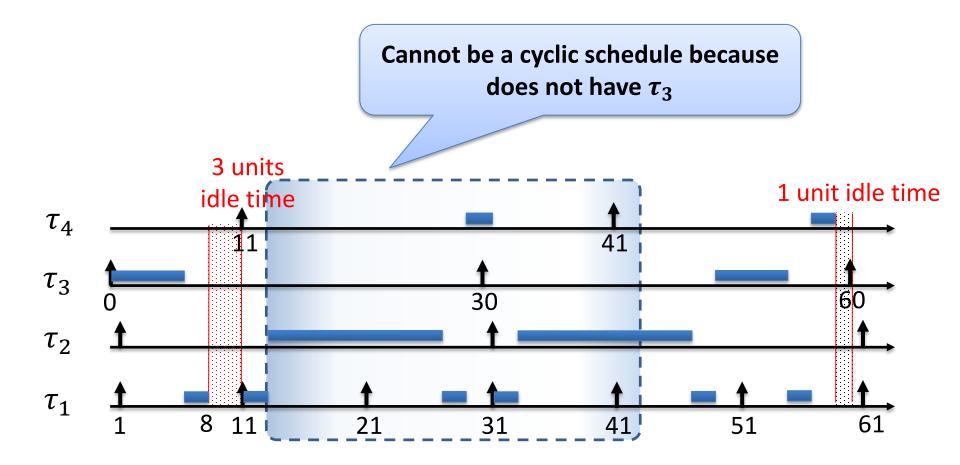
# **Looking Closer**

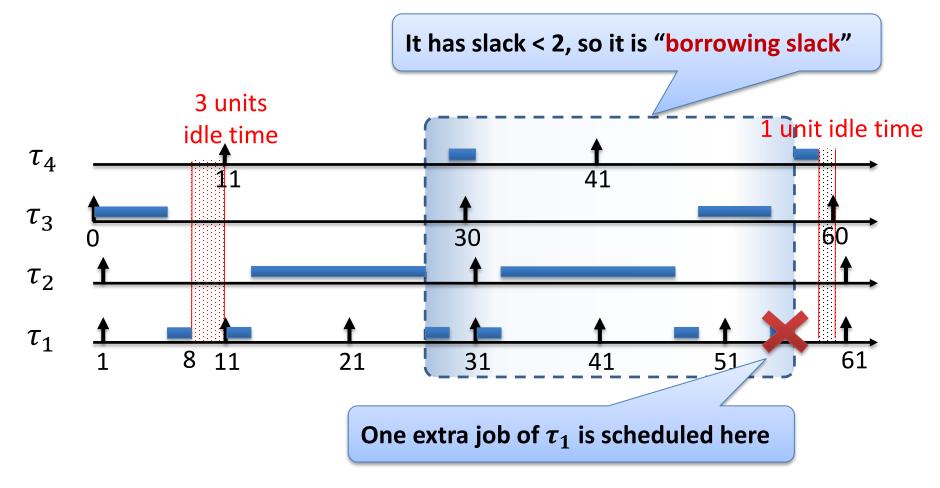












### Non-preemptive Scheduling is a Way to Increase Predictability

- It increases the predictability
  - Better estimation of the WCET
  - More accurate cache analysis
  - More accurate information about accesses to shared data

#### It is inevitable in many systems

- GPUs
- CAN networks
- small embedded systems



### It simplifies design and reduces overheads

Resource management becomes easier